

Abstract of the Disclosure

In the display area of the TFT array substrate, a pixel electrode, a switching element connected to the pixel electrode, a gate line connected to the switching element and a source line connected to the switching element are formed. In the terminal forming area of the TFT substrate, a terminal electrode for connecting the gate line or source line to external signal source is formed. Around the terminal forming area, a first metallic line and a second metallic line are extended below the terminal electrode. The first metallic line and the second metallic line are connected to the terminal electrode via respective contact holes. The first metallic line and the second metallic line are in different layers interposing an insulating layer therebetween.

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